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Title: CA2287413AA: ACCELERATEUR DE MATERIEL POUR LANGAGE D PROGRAMMATION ORIENTE OBJET

Country: CA Canada
Kind: AA Laid-open Application i

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Assignee: IREADY CORPORATION United States of America
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Published / Filed: 1998-11-12 / 1998-04-30

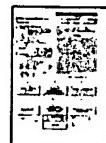
Application Number: CA1998002287413

IPC Code: G06F 9/38;

ECLA Code: None

Priority Number:
 1997-05-08 US1997000045951P
 1997-11-06 US1997000965540
 1998-04-30 WO1998000008719

Abstract:
 A method and apparatus for accelerating an object-oriented programming language are provided at a hardware gate level. In a Java-compliant embodiment, a Java Application framework is implemented in hardware. The Java.awt, Java.NET, and Java.IO application frameworks are supported in the preferred embodiment of the invention. Application framework classes are stored as libraries in a shared memory. Instances and methods of supported application framework classes that are executed by a Java program are offloaded to a hardware object management system. A software stub is provided as an interface between the hardware object management system and the central processing unit. Additional application frameworks can be supported by modifying or replacing the software stub. Hardware object management system requests are executed by an application framework-specific hardware accelerator. Application framework classes are retrieved from the shared memory as needed, and executed instructions are stored to the shared memory to be accessed by the central processing unit. Central processing unit processing of non-supported application framework instructions is continued during hardware accelerator execution of hardware object management system requests.


INPADOC Legal Status:

Gazette date	Code	Description (remarks)	List all possible codes for
2003-04-03	AFNE +	National phase entry (1999-10-19)	

2003-04-03 | EER + Examination request (1999-10-19)

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Family:

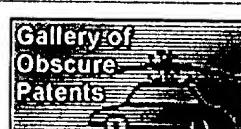
PDF	Publication	Pub. Date	Filed	Title
<input type="checkbox"/>	WO9850852A1	1998-11-12	1998-04-30	HARDWARE ACCELERATOR FOR OBJECT-ORIENTED PROGRAMMING LANGUAGE
<input type="checkbox"/>	US20020078115A1	2002-06-20	2001-06-20	Hardware accelerator for an object-oriented programming language
<input type="checkbox"/>	US6330659	2001-12-11	1997-11-06	Hardware accelerator for an object-oriented programming language
<input checked="" type="checkbox"/>	JP2000514944T2	2000-11-07	1998-04-30	
<input type="checkbox"/>	EP0980544A1	2000-02-23	1998-04-30	HARDWARE ACCELERATOR FOR OBJECT-ORIENTED PROGRAMMING LANGUAGE
<input checked="" type="checkbox"/>	CN1266512T	2000-09-13	1998-04-30	HARDWARE ACCELERATION FOR OBJECT-ORIENTED PROGRAMMING LANGUAGE
<input checked="" type="checkbox"/>	CA2287413AA	1998-11-12	1998-04-30	HARDWARE ACCELERATOR FOR OBJECT-ORIENTED PROGRAMMING LANGUAGE
<input checked="" type="checkbox"/>	AU7364898A1	1998-11-27	1998-04-30	HARDWARE ACCELERATOR FOR OBJECT-ORIENTED PROGRAMMING LANGUAGE
<input checked="" type="checkbox"/>	AU0734115B2	2001-06-07	1998-04-30	HARDWARE ACCELERATOR FOR OBJECT-ORIENTED PROGRAMMING LANGUAGE

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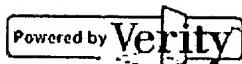
Other Abstract

None

Info:



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